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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of:

Chung, et al.

Serial No.: 09/965,370

Confirmation No.: 6507

Filed:

09/26/2001

For:

Integration Of Barrier Layer

And Seed Layer

MAIL STOP Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Group Art Unit: 1762

Examiner:

Unknown

CERTIFICATE OF MAILING 37 CFR 1.8

I hereby certify that this correspondence is being deposited on June 27, 2003 with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

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## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

The Applicants, and the Attorney who signs below on the basis of the information supplied by the inventor and the information in his file, submit herewith patents, publications, or other information of which they are aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

While the information submitted in this Supplemental Information Disclosure Statement may be material pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication, or other information referred to therein is prior art for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97, this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or

Form PTO-1449.

If the sum of \$180.00 is due under 37 CFR § 1.17(p) pursuant to § 1.97, the Commissioner is hereby authorized to charge this fee, and any other fee necessary to make this submission timely, to the Deposit Account No. 20-0782/AMAT/6303/AOP.

Respectfully submitted,

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Sheet 1 of 1 sheet(s) U.S. Department of Commerce, Patent and Trademark Office Docket No. Serial No. (PTO Form 1449 modified) AMAT/6303 09/965.370 INFORMATION DISCLOSURE STATEMENT BY APPLICANT Confirmation No.: **Applicant** 6507 Chung, et al. (Use several sheets if necessary) Filing Date Group Examiner Unknown 09/26/2001 1762 **U.S. Patent Documents** \*Examiner Document Issue Applicant(s) Class Subclass Filing Date If Initial Number Date Name Appropriate **A1** 5,529,955 06/25/1996 Hibino, et al. 437 195 08/25/1994 A2 A3 Α4 A5 Α6 A7 8A Α9 A10 A11 A12 A13

## **Foreign Patent Documents**

*Examiner Initial		Document	Date	Country	Class	Subclass	Translation	
		Number					YES	NO
	B1							
	B2							
	B3							

## **OTHER ART**

*Examiner Initial		Including Author, Title, Date, Pertinent Pages, Etc.			
	C1	R.C. Ellwanger, "An Integrated Aluminum/CVD-W Metallization Process For Sub-Micron Contact Filling", June 11-12, 1991 IEEE Catalog No. 91 TH0359-0, pages 41-50.			
	<u>C</u> 2	Kaanta et al. "Dual Damascene: A LII SI Wiring Tochnology", June 11, 12, 1001 VMIC			

Examiner

**Date Considered** 

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant